

CPEN 230L: Introduction to Digital Logic Laboratory

Lab Procedures

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Lab Preparation

- Most labs require pre-lab work. Doing pre-lab work before the lab session is mandatory. This leaves time for mistakes and re-work, or help from the instructor and other. Lab handouts are typically available 6 days before the lab session for this purpose. Typically, the pre-lab work has a number of deliverables that needs to be handed in before the lab starts. Failing to do so will result in a 50% penalty of the lab's grade.
- Each lab builds on the concepts of previous labs as well as CPEN 230 lectures. Don't fall behind.

Grades

- Each lab will be worth 10 points, except for the final project that will be worth 30. Correct results account for 70% of each lab. The remaining 30% goes toward good report content, as detailed in the Lab Report Template.
 - Rationale for the 30% value: Being able to communicate well is vital to any engineering job. Use this class as an opportunity to practice! Grammar and spelling matter. Proofread each report before submission.
 - Attendance is mandatory. If you miss a lab without prior discussion with the instructor, you will be required to make up the lab subject to a 30% penalty in grade. No penalty will be imposed if an arrangement is made with the instructor prior to the lab.
 - Getting Help (good) versus Copying (bad)
 - Helping each other before and during the lab session is strongly encouraged. Feel free to exchange concepts, information pointers, tips and tricks, etc. But all your deliverables must be completely generated by you – pre-lab work, breadboard circuits, schematics, Verilog code, tables, images... everything in your report.
 - Even if you work in a group, you should submit your own report with your own words.

Lab Reports

- Pre-lab deliverables are due (in print) before the lab starts.
- Lab reports are due in (print) at the very beginning of the following lab.