

# IEEE Distinguished Lecturer Program

Dr. Jake Baker

Professor, Electrical & Computer  
Engineering, University of Nevada, Las Vegas



## “Design, Layout and Simulation of Operational Amplifiers”

Thursday, April 9<sup>th</sup> | 5-7pm | Jepson 006

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Friday, April 10<sup>th</sup> | 9-11am | Wolff Auditorium (Jepson 114)

Baker received his B.S. and M.S. degrees in electrical engineering from the University of Nevada, Las Vegas, in 1986 and 1988. He received his Ph.D. degree in electrical engineering from the University of Nevada, Reno in 1993. From 1985 to 1993 he worked for E. G. & G. Energy Measurements and the Lawrence Livermore National Laboratory designing nuclear diagnostic instrumentation for underground nuclear weapons tests at the Nevada test site. From 1993 to 2000 he served on the faculty in the department of electrical engineering at the University of Idaho. In 2000 he joined a new electrical and computer engineering program at Boise State University where he served as department chair from 2004 to 2007. At Boise State he helped establish graduate programs in electrical and computer engineering including, in 2006, the university's second PhD degree. In 2012 he joined the faculty at the University of Nevada, Las Vegas where his research focuses on the design of diagnostic instrumentation for scientific research, integrated electrical/biological circuits and systems, array (memory and displays) circuit design, low-power interconnect techniques, communication circuit design, and the delivery of online engineering education. He also currently serves on, or has served on, university advisory boards, the IEEE Press Editorial Board (1999-2004), as editor for the [Wiley-IEEE Press Book Series on Microelectronic Systems](#) (2010-present), on the IEEE Solid-State Circuits Society (SSCS) Administrative Committee (2011-2016), as the Technology Editor (2012-2014) and Editor-in-Chief (2015 - present) for the [IEEE Solid-State Circuits Magazine](#), as a Distinguished Lecturer for the SSCS (2013-2014), and as the Technical Program Chair for the IEEE 58<sup>th</sup> 2015 International Midwest Symposium on Circuits and Systems, [MWSCAS 2015](#).