

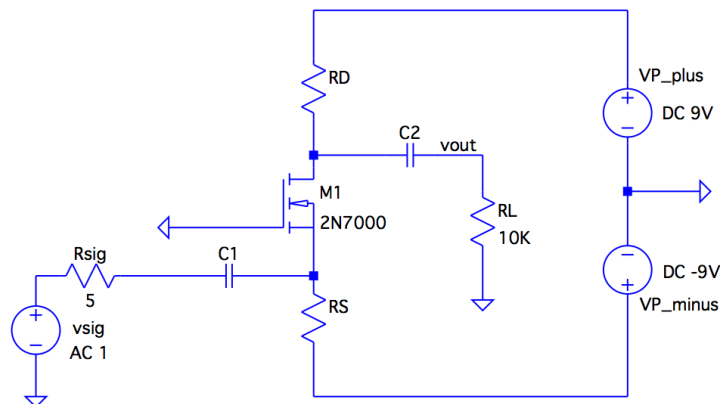
Objectives:

- Performing DC and small signal AC analysis of the circuit
- Simulating the circuit and comparing the simulation results with hand analysis
- Implementing the circuit in an experimental setting, taking measurements and comparing the circuit behavior with theoretical and simulated results

Pre-Lab

Consider the following amplifying circuit using a 2N7000 nMOST. The SPICE model of the 2N7000 is as follows:

```
.SUBCKT 2N7000 3 4 5
* Nodes      D G S
M1 3 2 5 5 MOD1
RG 4 2 343
RL 3 5 6E6
D1 5 3 DIODE1
.MODEL MOD1 NMOS VTO=2.474 RS=1.68 RD=0.0 IS=1E-15 KP=0.296
+CGSO=23.5P CGDO=4.5P CBD=53.5P PB=1 LAMBDA=267E-6
.MODEL DIODE1 D IS=1.254E-13 N=1.0207 RS=0.222
.ENDS 2N7000
```



Design the circuit to achieve a small signal gain of at least $A_v = v_{out}/v_{sig} = 5$ V/V. Assume the following specifications: $R_L=10K\Omega$, $R_{sig}=50\Omega$, $V_{P_plus}=9V$, $V_{P_minus}=-9V$. Use a matlab script to carry on all the required computations.

1. DC operating point analysis
 - a. Sketch the DC model of the circuit

- b. Make an adequate design choice for the value of the DC drain current

$$I_D = \underline{\hspace{2cm}}$$

- c. Calculate V_{ov} (show the symbolic equation you used)

$$V_{OV} = \underline{\hspace{2cm}}$$

- d. Calculate g_m (show the symbolic equation you used)

$$g_m = \underline{\hspace{2cm}}$$

- e. Calculate V_{GS} (show the symbolic equation you used)

$$V_{GS} = \underline{\hspace{2cm}}$$

- f. Calculate r_o (show the symbolic equations you used)

$$r_o = \underline{\hspace{2cm}}$$

- g. Calculate R_S (show the symbolic equations you used)

$$R_S = \underline{\hspace{2cm}}$$

2. AC analysis

- a. Sketch the small-signal model of the circuit:

- b. Calculate the ratio v_s/v_{sig} (show the symbolic equation you used)

$$v_s/v_{sig} = \underline{\hspace{2cm}}$$

- c. What is the value of R_D that produces $v_{out}/v_{sig} = 5$ V/V?

$$R_D = \underline{\hspace{2cm}}$$

- d. How much is the DC voltage at the drain?

$$V_D = \underline{\hspace{2cm}}$$

3. Attach the matlab script you used to carry on all required computations

Lab

1. Simulate the circuit with SPICE:

<i>Parameter</i>	<i>SPICE value</i>	<i>Theoretical value</i>	<i>% Error</i>
VGS =			
VDS =			
ID =			
VOV =			
VTH =			
gm =			
ro=1/go =			
Av = vout/vsig =			
BW =			

$\% \text{ error} = 100 \times (\text{spice} - \text{theoretical}) / \text{theoretical}$

2. What SPICE analysis did you run to obtain the data in the table above (write the exact SPICE command you used):

Attach the corresponding plots/tables.

a.

b.

3. Build the given amplifier and measure the DC operating point.

<i>Parameter</i>	<i>SPICE value</i>	<i>Measured value</i>	<i>% Error</i>
VGS =			
VDS =			
ID =			

4. Use the AWG (Arbitrary Waveform Generator) to generate the signal vsig to be applied at the input of the amplifier. Make vsig a sinusoidal waveform.

What is the amplitude of vsig you selected?

amplitude = _____

Explain shortly how did you select the amplitude?

What is the frequency you selected for vsig?

freq = _____

Explain shortly how did you select the amplitude?

Use the oscilloscope to measure the amplitude of vout (that is the small signal voltage taken across RL).

What is the amplitude of vout?

amplitude = _____

vout amplitude / vsig amplitude = _____

5. Use the network analyzer to measure the gain and bandwidth of the amplifier.
 - a. Attach the Bode plot.

- b. Compare the values measured with the values you obtained using SPICE:

<i>Parameter</i>	<i>SPICE value</i>	<i>Measured value</i>	<i>% Error</i>
Gain = $20 \text{ Log } (A_v) =$			
BW =			