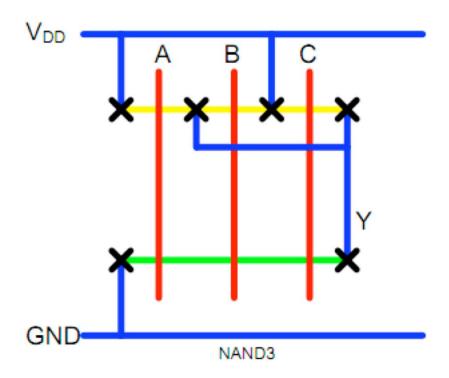
## **Stick Diagrams: examples**

Stick diagrams help plan layout quickly

- Need not be to scale
- Draw with color pencils or dry-erase markers

## **Example: NAND3**



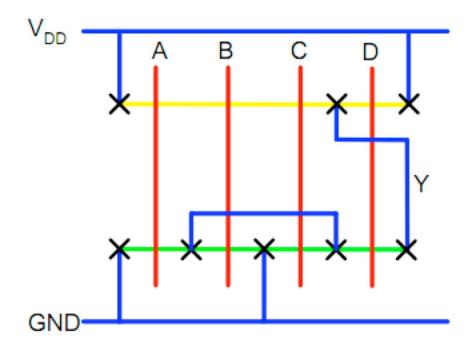


VxH = 40x32

## **Example: O3AI**

pdiff

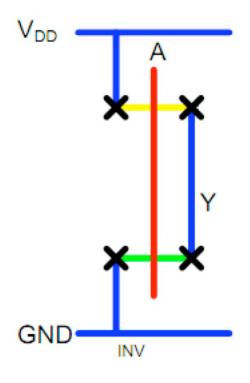
$$Y = \overline{(A+B+C) \cdot D}$$



$$VxH = 48x40$$

## **Example: INV**





Always pretend there is a track between the nMOS and the pMOS

VxH = 40x16

VxH = 32x16

Are you sure ??